

Internship Proposal

Knowledge Compilation and Enumeration for Efficient Query Evaluation Algorithms

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Location: INRIA Lille, Spirals Team; Lille, France; or Télécom Paris, DIG Team; Palaiseau, France

General background. Over the past decades, the database and knowledge representation communities have converged to expressive formalisms to describe queries and reasoning. They have studied how to evaluate complex queries on large databases [2] and perform reasoning at scale in the context of *ontology-mediated query answering* [5, 8, 10].

However, even if these approaches are very efficient in the size of the input data, the set of query answers to compute can be prohibitively large. For this reason, the database theory community has focused in recent years on efficient *enumeration* of query results. In this approach, the database is first preprocessed to compute a compact representation of all solutions. Second, the solutions can be enumerated one after the other, with a small *delay* between answers. This fine study of complexity makes it possible to design algorithms that can return a first answer, or the first few answers, as efficiently as possible.

In 2017, we proposed [3] a framework for efficient enumeration algorithms, via the field of *knowledge compilation*. This area studies families of Boolean and arithmetic circuits under different syntactic and semantic restrictions, to ensure the tractability of basic operations on these circuits. We showed that enumeration algorithms could be designed in a modular fashion. First, express the set of all solutions as a circuit in a tractable knowledge compilation formalism. Second, design general-purpose enumeration algorithms on them. The point is that circuits serve as an intermediate representation that neatly separate the algorithmic problems of enumeration from the logical problems of reasoning and query evaluation. Further, circuits can also serve for other tasks, e.g., explanation of query results [7, 4], counting, etc.

Goals of the internship. The goal of this internship is to expand this approach to the latest developments in reasoning and query evaluation. Specifically, one first goal is to design circuit representations and enumeration algorithms for recursive query languages, such as regular path queries (already studied recently in [6]) and Datalog [1]. A second goal is enumeration for query evaluation techniques based on submodular width [2], to generalize the study done for fractional hypertree width in the context of factorized databases [9].

Environment and supervision. The internship will last for 4–6 months and be supervised by Pierre Bourhis¹ (CNRS) and Antoine Amarilli² (associate professor at Télécom Paris), in the context of the ANR CQFD project. The internship can be located in INRIA Lille or at Télécom Paris in Palaiseau (south of Paris), to be discussed with the applicant. This internship can lead to a PhD thesis funded as part of the CQFD project.

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References

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